

**Notice of References Cited**

Application/Control No.

09/843,533

Applicant(s)/Patent Under  
Reexamination  
FORNACIARI ET AL.

Examiner

Kim T. Huynh

Art Unit

2112

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**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
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	C	US-			
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**FOREIGN PATENT DOCUMENTS**

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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
*	U	Ramprasad et al., "A Coding Framework for low-power address and data busses", IEEE Transactions on VLSI, Vol.7(2), pp212-221
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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